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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10045376	11/09/2001	438	347	2812	

**APPLICANTS: Doyle Brian; Roberds Brian;

**CONTINUING DATA VERIFIED:

THIS APPLICATION IS A DIV OF 09/342,030 06/28/1999 PAT 6,362,082

** FOREIGN APPLICATIONS VERIFIED: N/A

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	ATTORNEY DOCKET NO
Verified and Acknowledged Examiner's initials		DL-	42390.P5768D
TITLE : Methodology for control of short channel effects in MOS transistors <i>New title</i>			
U.S. DEPT. OF COMM./PAT. & TM-PTO-436L(Rev. 12-95)			

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims 16	Print Claim for O.G 1
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs.Drwg.
TERMINAL DISCLAIMER		PREPARED FOR ISSUE		
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